

THOMAS C. DETWEILER
TEST/MANUFACTURING ENGINEER
530-273-5588

Test Engineering, Production and Manufacturing Engineering, Component Engineering

Multidisciplinary, multitasking, organized Test & Manufacturing Engineer with experience in low to high volume production OEM, Contract, and Subcontract Manufacturing.

Dependable, organized, responsible, with existing team, project management, customer and vendor interface, communication and documentation skills necessary to maintain and improve customer relationships;

Skilled at justifying and implementing cost-effective test and manufacturing methods.

Support manufacturing for high-density mixed-technology SMT PCBAs, wire harnesses, subassemblies;

Provided Customer support for cost effective manufacturing of board and assembly-level products;

Supported offshore Contract Manufacturing assembly and test transition, to Hong Kong and Singapore;

Supported customer implementation of automated Audio and Video Technology testing at board level;

Helped maintain Test Technician staffing including interviewing, and OJT/in-house training of techs.

Teamed with Design/CAD/Component groups and customers to produce testable cost-reduced PCBs;

Work with purchasing, customer engineering and suppliers, to obtain replacements for end of life components;

Managed contract assembly and test vendors, subcontractors and consultants, fixture/tooling suppliers, tester maintenance,

ATE after-sales, apps and field engineers; managed process transfers to HK Contract Manufacturing PCB facility;

Conversant with IPC, IEEE, UL/CSA and TUV organizations, standards groups, audits and certifications;

Planned, scheduled, tracked, implemented ECOs, QC and QA of Test operations; Manufacturing drawings;

Know most familiar PC-based applications, word processing and database management, documentation; performed software upgrades and installation, complex hardware upgrades; instrument maintenance and repairs as needed.

TECHNICAL SKILLS and PROFICIENCIES:

Read and interpret complex schematics or wiring diagrams; research component data and related activities;

ATE Test: GenRad, Factron, Teradyne, HP In-Circuit test ATE, Checksum MDA; Cirris Cable Tester programming;

ICT/MDA operator/technician training and certification, for OEM, EMS and customer operations;

IEEE-488/GPIB in Basic/Test Windows; developed cost-effective Bench Functional tests;

Advanced test strategy development for IC device, System On Chip, PCBA, subassemblies and Systems;

Developed LSI device tests, libraries, interfaces, translators in various interpreter/compiler, sequential or structured tester environments from embedded code to applications, in PC and Unix operating systems;

Team with Component Engineering to implement IEEE-1149 Boundary Scan, JTAG, and BIST test methods;

Testability and Design for Test Analysis from design specification to finished board or system level product;

ISO critical team member in two successful ISO certifications, upgrades and audits;

Managed ISO-compliant metrology and instrument Calibration, repairs, and upgrades;

Specified and procured UL1950/CSA/TUV Hi-Pot and Ground Bond automated Testing equipment, procedures, training;

Implemented Cybortronics 3-bay programmable Elevated Temperature burn-in oven and controllers;

CAD training for Design for Test/Manufacturing; implemented board level DFT to customer and IPC standards;

Import CAD data to ICT, ATE, MDA testers, for internal/external application;

Networked ATE to factory information systems; implemented Bar Code system on ATE and technician work stations;

Planned, implemented US-HK dedicated network server to transfer yield data for SPC remote management;

Specification, support documentation, test defect data and pass/fail yield reporting and graphing;

EMPLOYMENT HISTORY

TESTOLOGY.COM, Grass Valley CA -- 2/03 to present: CheckSum MDA programming, Test consulting self employment

VARIAN VEM, Rocklin CA; 10/00 - 1/30/03: Functional Test/ICT/MDA/ Support Engineer/subcontract manager, Test Engineer.

JONES EMS/IMAGINE MFG INC., Rocklin CA; 5/94-10/00: Test/ICT/MDA Engineer/subcontract manager, Test supervisor.

TEST CONSULTANT (Independent); 1993-1994: GenRad and Factron ICT contract development and consulting.

MAXTOR CORP., San Jose CA; 1985-1993: Sr. Test Engineer, In-Circuit Test/Manufacturing Dept.

EDUCATION:

200+ hours Fairchild, Zehntel/Teradyne, Genrad, HP ICT/ATE, Cadnetix CAD, GC-Place, CIMbridge, factory training;

In-house IPC training, product seminars, project and Franklin time management; Z80 assembler, Manx C, BASIC, Forth,

TestWindows, GPIB, Boundary Scan and JTAG test training; SAP and SAP/QM training; ISO and UL Certification training.

BS, Eastern Mich. University, Ypsilanti MI -- Sciences; 1978.

AA, Washtenaw Community College, Ann Arbor MI -- 1976.

U.S. Navy Technical A/B Schools, Electronics and Electro-Mechanical -- 1968.

PROFESSIONAL ASSOCIATIONS: American Society of Test Engineers (ASTE); ATE User Group concurrent to tester.

(Renewable) International Passport

Business, Personal References furnished to serious prospective employers upon request.

THOMAS C. DETWEILER
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